

Jackson Parrack

(636) - 697 - 6605 | parrackjack@gmail.com | www.linkedin.com/in/jackson-parrack | <https://github.com/rojoloco04>

Education

Bachelor of Science in Computer Engineering

Saint Louis University | Saint Louis, MO

May 2026

Minor in Computer Science

GPA: 3.92/4.0

Sogang University | Seoul, South Korea | Exchange Program

Spring 2024

Relevant Coursework

Quantum Information Science and Computing, Embedded Systems, Data Structures and Algorithms, Computer Architecture, Computer Networks, Electrical and Electronic Circuits, Digital Design

Extracurricular Activities

- School of Science and Engineering First-Year Mentor *Aug. 2024 - Present*
- Institute of Electrical and Electronics Engineers (IEEE) *Aug. 2022 - Present*
 - Vice President *Aug. 2025 - Present*
 - Assisted with chapter leadership in events, finance, and member recruitment/retention.
- SLU Esports *Aug. 2022 - Present*
 - President and Teams Coordinator *Aug. 2024 - Present*
 - Oversaw board operations and scheduling for more than 10 club teams.
 - Increased membership to over 900 by improving leadership and engagement.

Work Experience

DevOps Intern | DataServ | Remote

May 2025 - Aug. 2025

- Leveraged custom Python automation scripts to migrate 3,000+ SQL stored procedures across 71 client environments into Git, allowing for easier future version control.
- Redesigned and implemented 17 post-import SQL configurations across 7 clients, replacing time-based jobs with event-driven triggers and validating reliability through full end-to-end testing.
- Optimized large-scale data ingestion processes, including a lookup redesign for 21 clients and multiple enterprise scheduling upgrades, improving system stability and performance.
- Delivered project demonstrations to leadership, communicated progress across teams, and collaborated on troubleshooting complex SQL and configuration issues in a fast-paced Agile environment.

Project Experience

Rapid Photonic Innovation Device (RAPID) | Capstone Project

Aug. 2025 - Present

- Reverse-engineered laser diode assemblies, spindle and stepper motors, and photodiode feedback using oscilloscopes and multimeters, and implemented motor and laser control logic in VHDL and C using an FPGA.
- Developed testing procedures and system integration plans involving PWM motor control, UART/USB communication, and vectorized user input interpretation, resulting in a functional prototype.

Vigilane | HackSLU

Feb. 2026

- Developed a cross-platform mobile app using React Native (Expo) and TypeScript for live road hazard reporting.
- Implemented user submission workflows and real-time data integration between frontend and backend services.
- Built a Flask REST API with Firebase Authentication and Firestore to manage reports, sessions, and data access.

Skills

- Programming Languages: C, C++, Python, TypeScript, JavaScript, SQL, Assembly, HTML/CSS
- Mobile: Android, iOS
- Cloud/DevOps: AWS, Git, Linux
- Hardware: VHDL, FPGA, ARM & AVR Microcontrollers, Arduino, Raspberry Pi
- Languages: Spanish (Advanced), Korean (Intermediate), Chinese (Beginner)